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**PATENT**  
Attorney Docket No.: 019717-002700US  
Client Reference No.: BP1884

On July 9, 2002

TOWNSEND and TOWNSEND and CREW LLP

By: DJ Bogorodsky  
DJ Bogorodsky

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Jun Cao

Application No.: 09/955,693

Filed: September 18, 2001

For: LINEAR PHASE DETECTOR FOR  
HIGH-SPEED CLOCK AND DATA  
RECOVERY

Examiner: Stephen Chin

Art Unit: 2634

INFORMATION DISCLOSURE  
STATEMENT UNDER 37 CFR §1.97 and  
§1.98

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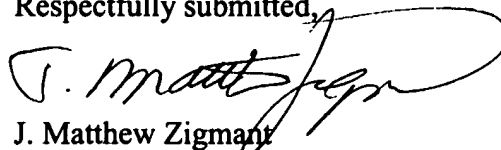
The references cited on attached form PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

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Respectfully submitted,



J. Matthew Zigmant  
Reg. No. 44,005

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FORM**

(to be used for all correspondence after initial filing)

|                        |                    |
|------------------------|--------------------|
| Application Number     | 09/955,693         |
| Filing Date            | September 18, 2001 |
| First Named Inventor   | Cao, Jun           |
| Group Art Unit         | 2634               |
| Examiner Name          | Stephen Chin       |
| Attorney Docket Number | 019717-002700US    |

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Remarks

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|                          |  |                 |
|--------------------------|--|-----------------|
| Firm and Individual name | Townsend and Townsend and Crew LLP<br>J. Matthew Zigmant | Reg. No. 44,005 |
| Signature                |  |                 |
| Date                     | July 9, 2002   |                 |

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PA 3229501 v1

Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 1

**Complete If Known**

Application Number 09/955,693

Filing Date September 18, 2001

First Named Inventor Cao, Jun

Art Unit 2634

Examiner Name Stephen Chin

Attorney Docket Number 019717-002700US JUL 22 2002

Technology Center 2600

**U.S. PATENT DOCUMENTS**

| Examiner | Cite No. <sup>1</sup> | Document Number                          | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines, Where<br>Relevant Passages or Relevant<br>Figures Appear |
|----------|-----------------------|--|--------------------------------|--|---|
|          |                       | Number Kind Code <sup>2</sup> (if known) |                                |  |   |
|          | AA                    | US-4,535,459                             | 08-13-1985                     | Hogge, Jr.   |   |
|          | AB                    | US-5,301,196                             | 04-05-1994                     | Ewen et al.  |   |
|          | AC                    | US-09/784,419                            | filed 02-15-2001               | Jun Cao  |   |

**FOREIGN PATENT DOCUMENTS**

| Examiner<br>Initials * | Cite<br>No. <sup>1</sup> | Foreign Patent Document   |  | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited<br>Document | Pages, Columns, Lines,<br>Where Relevant<br>Passages or Relevant<br>Figures Appear | T <sup>6</sup> |
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|                        | AE                       |                           |  |                                |   |  |                |

**OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS**

| Examiner<br>Initials * | Cite<br>No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|------------------------|--------------------------|---|----------------|
|                        | AF                       | C.R. HOGGE "A self-correcting Clock Recovery Circuit", IEEE Journal of Lightwave Technology, vol. LT-3, pp. 1312-1314, Dec. 1985  |                |
|                        | AG                       | E. MULLNER, "A 20 Gb/s Parallel Phase Detector and Demultiplexer Circuit in a Production Silicon Bipolar Technology with $f_T=25$ GHz", Proc. IEEE BCTM, pp. 43-45, Oct. 1996   |                |
|                        | AH                       | M. RAU et al., "Clock/Data Recovery PLL Using Half-Frequency Clock", IEEE Journal of Solid-State Circuits, vol. 32, no. 7, July 1997  |                |
|                        | AI                       | K. NAKAMURA et al., "A 6 Gbps CMOS Phase Detecting DEMUX Module Using Half-Frequency Clock", IEEE, 1998 Symposium on VLSI Circuits Digest of Technical Papers   |                |
|                        | AJ                       | M. WURZER et al., "40 Gb/s Integrated Clock and Data Recovery Circuit in a Silicon Bipolar Technology", IEEE, 1998  |                |
|                        | AK                       | JAFAR SAVOJ et al., "A 10-Gb/s CMOS Clock and Data Recovery Circuit", IEEE, 2000 Symposium on VLSI Circuits Digest of Technical Papers  |                |

Examiner  
SignatureDate  
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<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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